



## Autosys Technologies

Build A1, Flat No 701, Nilay, Gat no 288,  
Jadhavwadi, Chikhali, Pune 412114,  
Maharashtra.

Email Id-technologyautosys@gmail.com

**Date- 28-11-2020**

Name- Sourabh Kashinath Futak  
Address- At post,-futakwadi, Tike, Ratnagiri  
Ratnagiri Maharashtra - 416712

Dear Mr. Sourabh Kashinath Futak

We are pleased to offer you the full-time position of **Junlor Design Engineer** at "Autosys Technologies" with a start date of 28<sup>th</sup> Nov. 2020, contingent upon your Immediate Joining. You will be reporting directly to Gajanan Kadam (Director) at Factory Site located at Talawade. We believe your skills and experience are an excellent match for our company.

In this role, your **Job duties & Responsibilities** are:

- Design, Engineer & Test automated machinery or processes that are intended to complete a specific task.
- Responsible for detailed design specifications and other documents in their creation.
- Interface with Management, other engineers and teams
- Design Engineers will provide detailed documentation including design specifications that enable production or the application of their products
- Help to streamline operations for efficiency, cost saving, safety and reliability.
- Maintain technical project files.
- Implementing and testing design concepts.
- Publishing reports documenting the design details of new projects and qualification test results.
- Co-ordinate with vendors to get the parts as per design.
- Quality control of each received part.
- Hands-on work with worker in Machine assembly & Testing.

